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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Aptina Imaging CorporationApplication No./Patent No.: 7,105,793 Filed/Issue Date: September 12, 2006Titled: CMOS PIXELS FOR ALC AND CDS AND METHODS OF FORMING THE SAME

Aptina Imaging Corporation, a Corporation  
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest in;
2. ☐ an assignee of less than the entire right, title, and interest in  
 (The extent (by percentage) of its ownership interest is \_\_\_\_\_ %); or
3. ☐ an assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made) the patent application/patent identified above by virtue of either:
- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

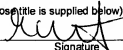
1. From: Rhodes, Howard E. To: Micron Technology, Inc.  
 The document was recorded in the United States Patent and Trademark Office at Reel 014622, Frame 0277, or for which a copy thereof is attached.
2. From: Micron Technology, Inc. To: Aptina Imaging Corporation  
 The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
3. From: \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet(s).

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

  
 \_\_\_\_\_  
 Signature  
 \_\_\_\_\_  
 Gianni Minutoli  
 Printed or Typed Name

\_\_\_\_\_  
 May 19, 2009  
 Date  
 \_\_\_\_\_  
 Attorney for Assignee  
 Title

## PATENT ASSIGNMENT AGREEMENT

### 1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED] Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/24/08, 2008

Notarization

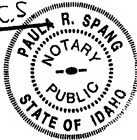
State of Idaho  
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton  
(date) (notary name)  
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED  
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Lauer Jr.

Date: 9/26/08, 2008

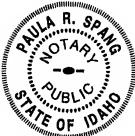
Notarization

State of Idaho  
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lauer, Jr.  
(date) (notary name)  
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

**ATTACHMENT "A"**

**IMAGING PATENTS**

## Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
7102826	AVGO-0034.00US	TELECENTRIC SINGLET HAVING A SMALL HEIGHT REQUIREMENT	United States of America	Sep 3, 2006
7105790	2003-0110.00US	CMOS PIXELS FOR ALU AND CDS AND METHOD OF FORMING THE SAME	United States of America	Sep 12, 2006
7112774	AVGO-0080.00US	CMOS STEREO IMAGING SYSTEM	United States of America	Sep 26, 2006
7115853	2002-1130.00US	MICRO LENS CONFIGURATION FOR SMALL LENS FOCUSING IN DIGITAL IMAGING DEVICES	United States of America	Oct 3, 2006
7110859	2002-1190.00US	IMAGE SENSOR HAVING PINNED FLOATING DIFFUSION DIODE	United States of America	Oct 3, 2006
7118923	2003-0241.00US	IMAGING WITH GATE CONTROLLED CHARGE STORAGE	United States of America	Oct 3, 2006
7118924	AVGO-0160.00US	PIXEL WITH ASYMMETRIC TRANSFER GATE CHANNEL	United States of America	Oct 3, 2006
7118991	2003-0806.00US	PACKAGED MICROELECTRONIC IMAGING DEVICES AND METHODS OF PACKAGING MICROELECTRONIC IMAGING DEVICES	United States of America	Oct 3, 2006
7116366	FBIT-0080.00US	CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	United States of America	Oct 3, 2006
7116368	FBIT-0089.02US	CMOS APS PIXEL SENSOR DYNAMIC RANGE INCREASE	United States of America	Oct 3, 2006
7118937	2003-0334.00US	WIDE DYNAMIC RANGE IMAGER WITH SUBPIXEL READOUTS	United States of America	Oct 10, 2006
7118921	2002-1550.00US	OPTICAL CHANNELS FOR MULTI LEVEL METAL OPTICAL IMAGERS	United States of America	Oct 10, 2006
7118932	2002-1190.01US	CMOS IMAGE SENSOR HAVING PINNED DIODE FLOATING DIFFUSION REGION	United States of America	Oct 10, 2006
7118936	FBIT-0032.00US	HIGH RESOLUTION CMOS CIRCUIT USING A MATCHED IMPEDANCE OUTPUT TRANSMISSION LINE	United States of America	Oct 10, 2006
7129457	1999-0112.01US	REDUNDANT IMAGING SYSTEMS	United States of America	Oct 31, 2006
7129458	AVGO-0131.00US	Wire-Bondable Image Sensor Having Integral Containment Shadowing Reduction Structure	United States of America	Oct 31, 2006